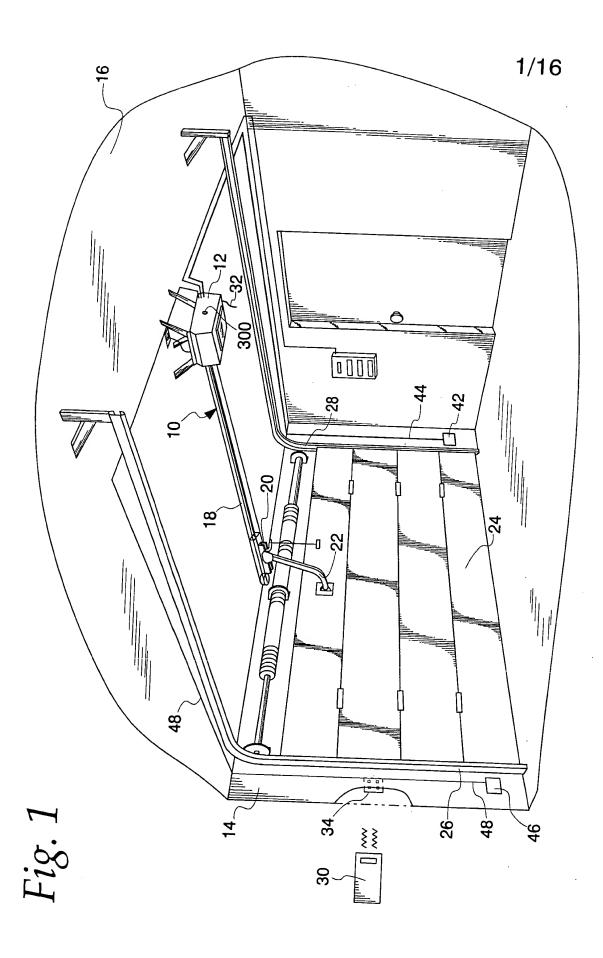
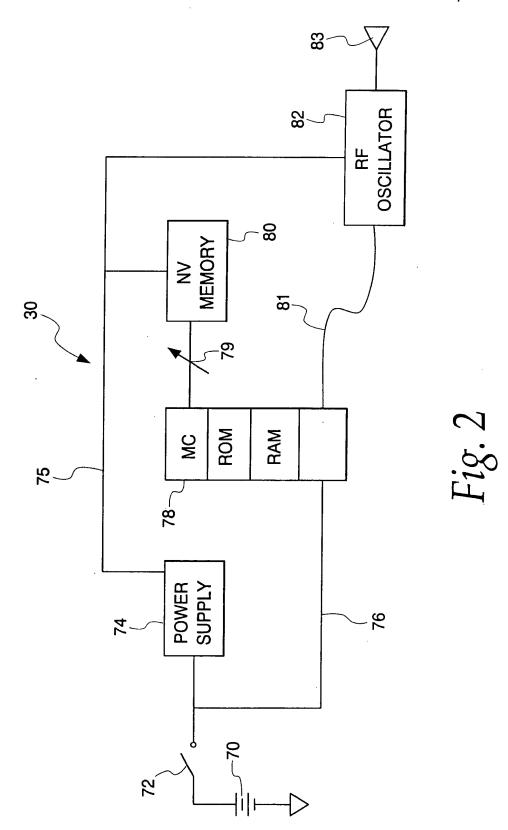
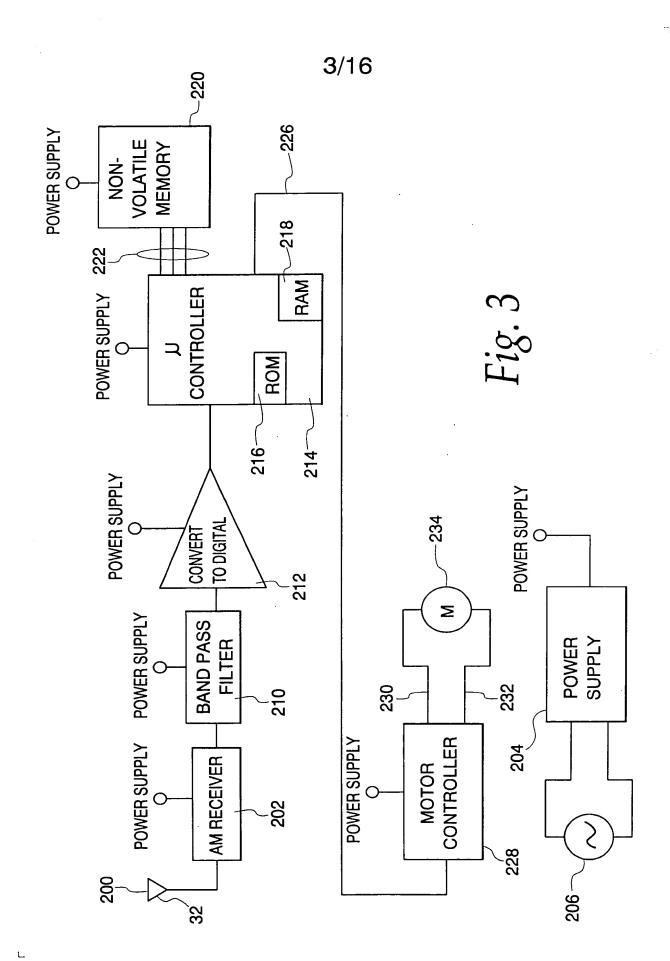
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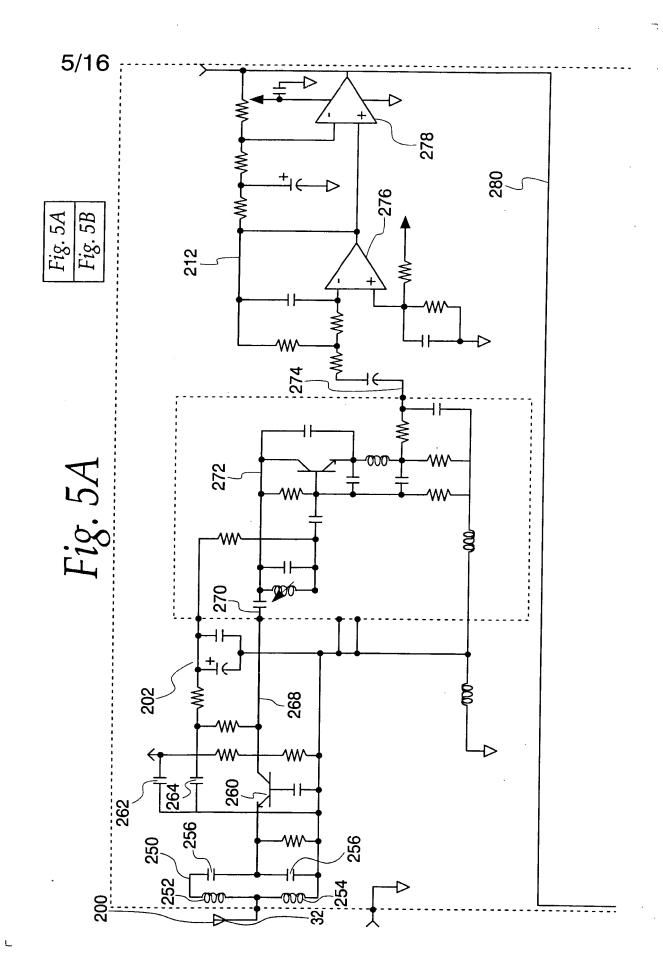


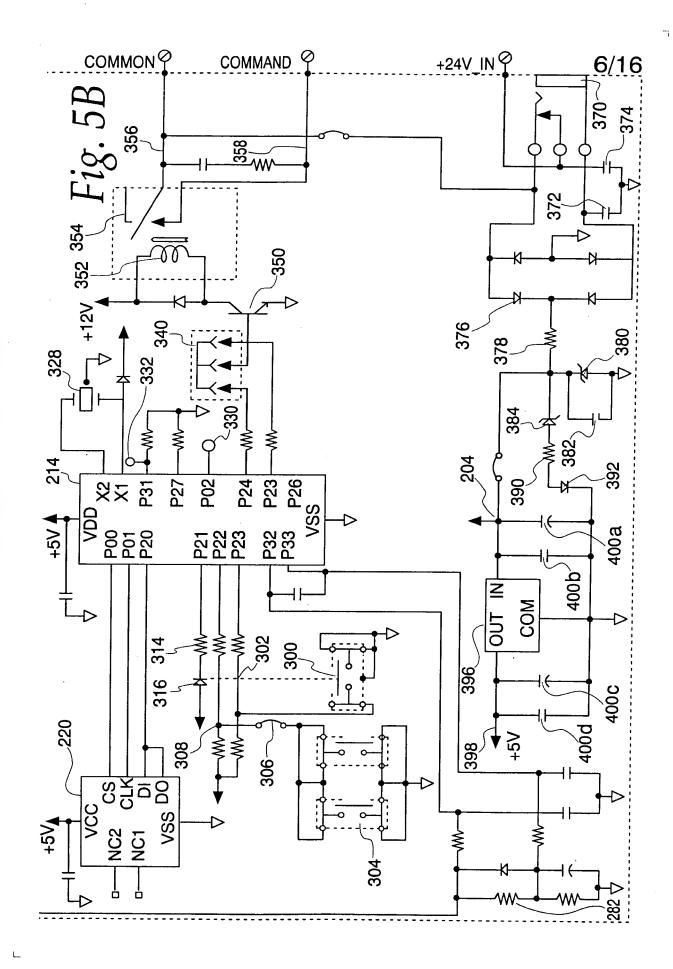
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ACODE B1 A CODE B2 A CODE B3 A CODE B4 A CODE B5 A CODE B7 A CODE B8 A CODE B9 A CODE B10

| B1 | B2 | B3 | B4 | B5 | B6 | B7 | B8 | B9 | B10 | B11 | B12 | B14 | B15 | B16 | B17 | B18 | B19 | B20 | BLANK 49 A CODE B11 A CODE B12 A CODE B13 A CODE B14 A CODE B15 A CODE B17 ID BIT 1 ID BIT 0 SWITCH 1D BLANK 48 B21 B22 B23 B24 B25 B26 B27 B28 B29 B30 B31 B32 B33 B35 B35 B36 B37 B38 B39 B40 BLANK 48 7/16 **ROLL B11** ROLL B1 LSB **ROLL B12** ROLL B2 ROLL B14 ROLL B13 ROLL B3 ROLL B4 ROLL B16 ROLL B15 ROLL B6 ROLL B5 **ACTIVE TIME - INACTIVE TIME BIT VALUE ROLL B17** ROLL B7 ROLL B19 ROLL B18 ROLL B8 ROLL B9 **ACTIVE** TIME SYNC 1.5 ROLL B10 SYNC 0.5 ROLL B20 MSB INACTIVE

Fig. 6

1.5 - 0.5 = 1.0 MILLISECONDS

0.5 mS

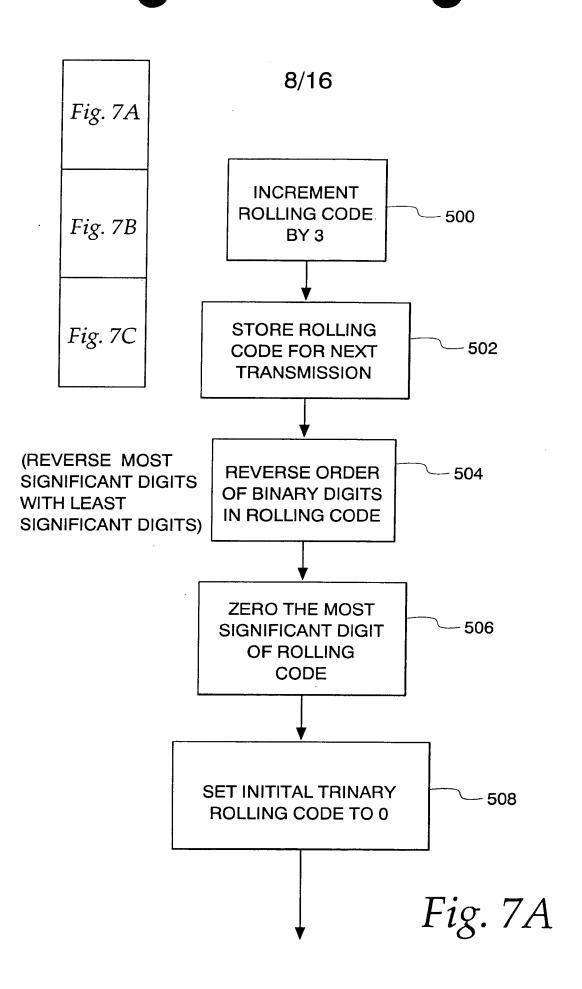
1.0 - 1.0 = 0 MILLISECONDS

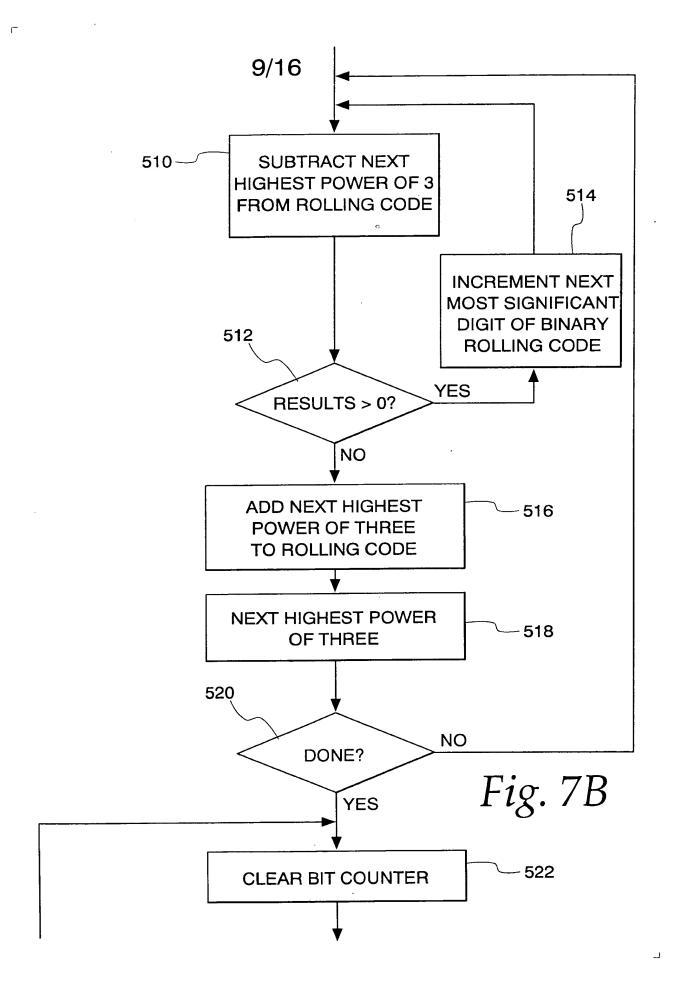
0.5 - 1.5 = -1.0 MILLISECONDS

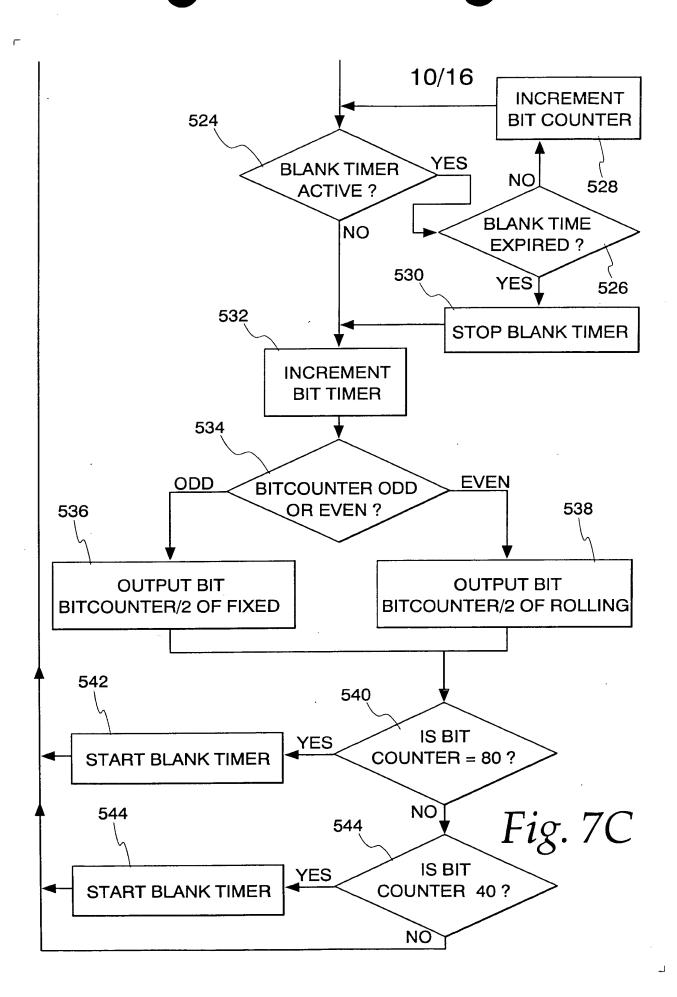
0.5 mS

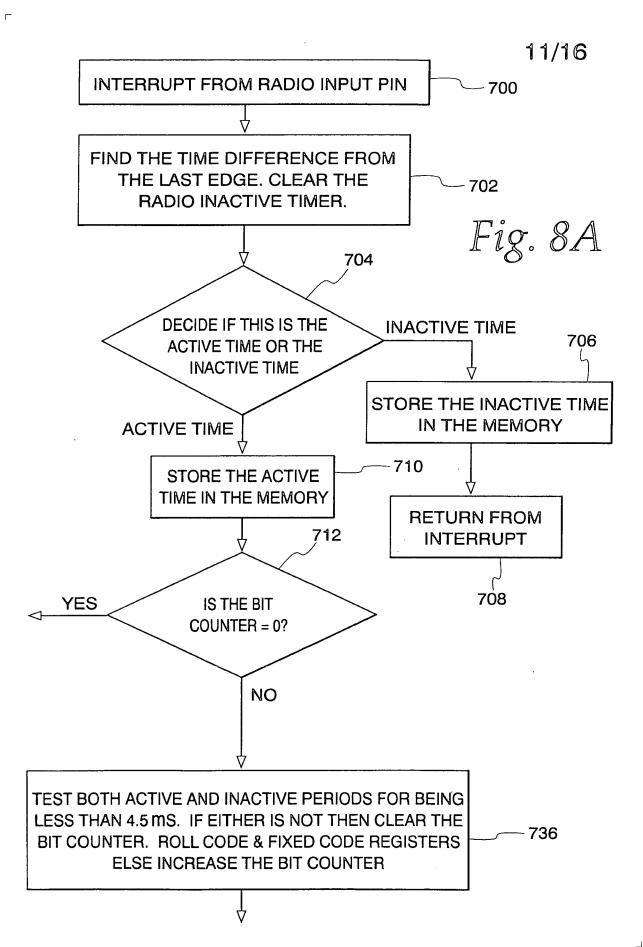
1.5 mS

**BIT TIMING** 







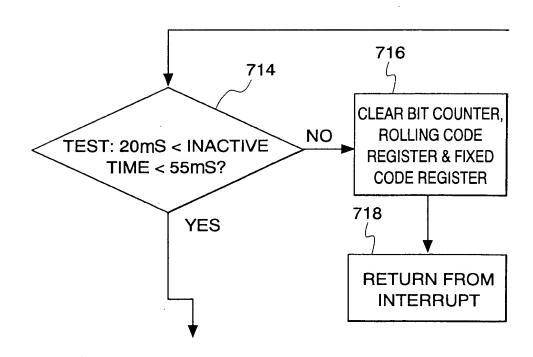


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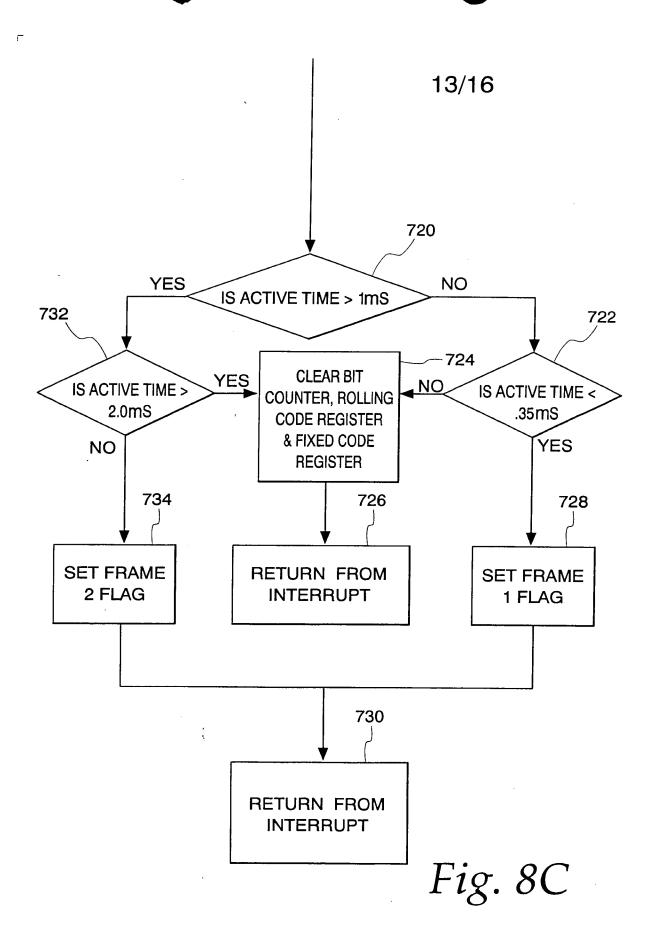
Fig. 8B	Fig. 8A
Fig. 8C	Fig. 8D
	Fig. 8E
	Fig. 8F

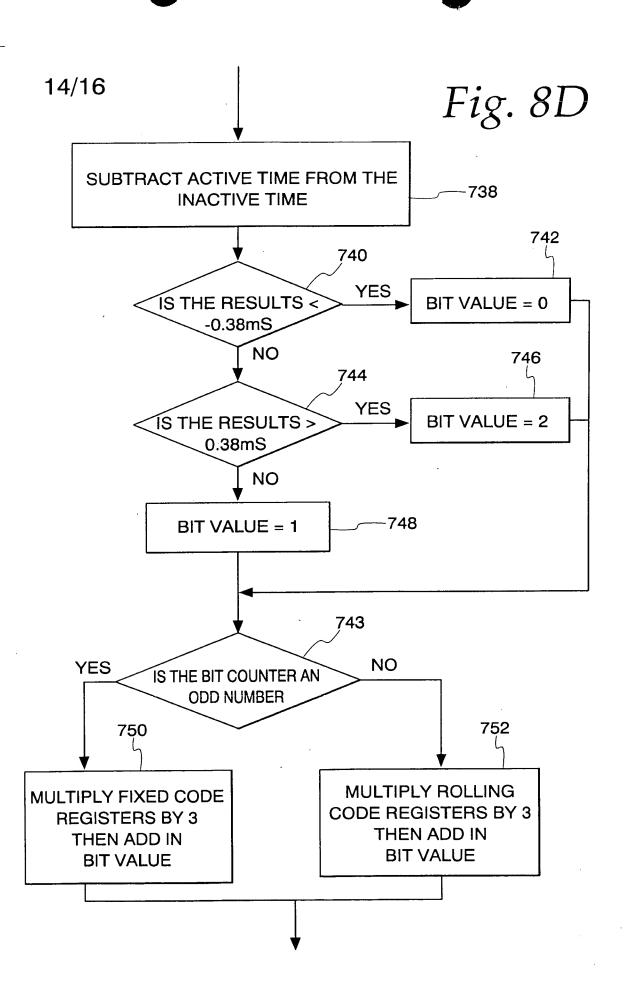
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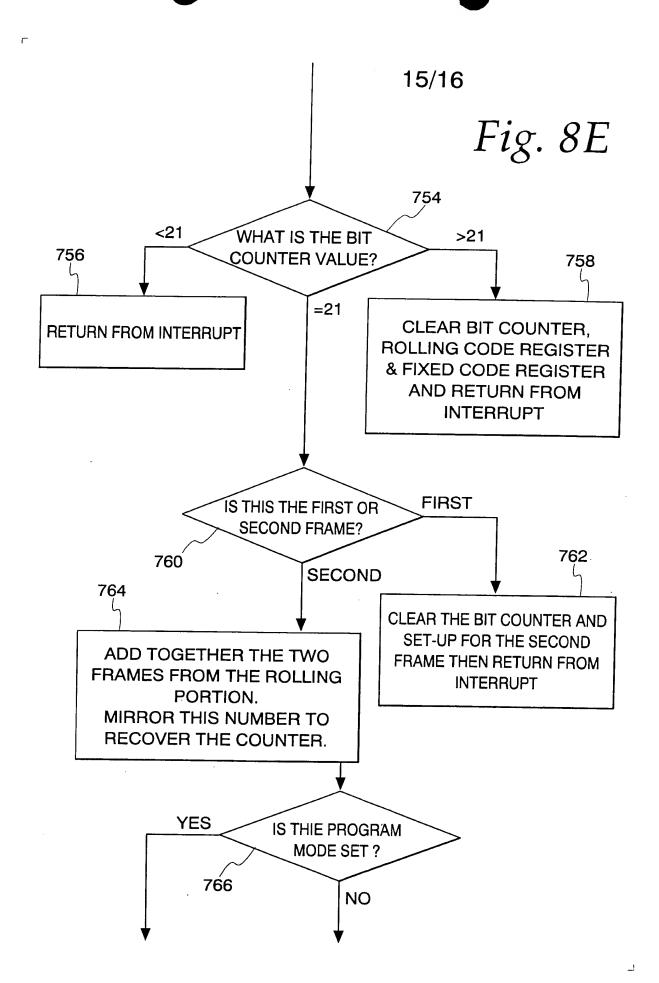
Fig. 8B



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